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Architecture And Memory

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(Part I) - Basic Logic

Design 4. Assembly Language

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Computer Architecture

Block Diagram

Architecture Of 8085

Microprocessor 8085

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made How to Make a

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Tutorial With Working

Mechanism Explained Part-2

Pin Diagram — 8085

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~~(Microprocessor) Internal~~

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Architecture

8085 Microprocessor The
Intel 8085 is an 8-bit
microprocessor introduced by
Intel in 1977. It is an
8-bit general purpose

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microprocessor capable of
addressing 64K of memory. It
is Single NMOS device. It
contains 6200 transistors
approx. The device has 40
pins Dual Inline-
Package(DIP), requires a +5V
single power supply, and can

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operate with a 3-Mhz clock.

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Microprocessor & Interfacing
(140701) Rahul Patel 1

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Architecture And Memory
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Rahul Patel, Assistant
Professor, EC Dept.,
Sankalchand Patel College of
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3 Chapter 4: 8085

Microprocessor Architecture
and Memory Interfacing 5 -
During the execution of the
instruction, these lines
carry the low-order address
bits during the early part

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of the cycle, then during
the later part of the cycle,
they carry the 8 data bits.

- This is known as
multiplexing the bus.

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Microprocessor Architecture
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Register Array • The
programmable registers were
discussed in the last
chapter • Two additional

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registers W and Z are
included in the register
array.

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4 Chapter 4: 8085
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and Memory Interfacing 7 The
Control and Status Signals •
IO/M: This is a status
signal used to differentiate
between I/O and memory
operations. – When it is

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high ($IO/M=1$), it indicates an I/O operation. - When it is low ($IO/M=0$), it indicates a memory operation.

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Chapter 5 The 8085
Microprocessor . 5.1
Architecture of 8085
Microprocessor . 5.1.1
Register Section . 5.1.2

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Address Buffer and Address-
Data Buffer . 5.1.3

Arithmetic and Logical Unit
(ALU)

(PDF) An Introduction to
Microprocessor 8085

- The microprocessor reads

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one instruction at a time,
matches it with its
instruction set, and
performs the data
manipulation specified. •

The result is either stored
back into memory or
displayed on an output

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device. 3. THE 8085
ARCHITECTURE • The 8085 uses
three separate busses to
perform its operations • The
address bus. • The data bus.
• The control bus. 4.

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And ...

After the request has been processed, the control shifts back to the main program. This interrupt

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might be a hardware or a software interrupt. Some interrupts can be ignored (or maskable) while some cannot (non-maskable). There are five interrupt signals in 8085 microprocessor:

INTR, RST 7.5, RST 6.5, RST

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5.5, TRAP. Increment or
Decrement Register

Understanding the 8085
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Introduction to
Microprocessors,
Microprocessor system with

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Microprocessor architecture
and operation, 8085

Microprocessor and its
operation, 8085 instruction
cycle, machine cycle, T
states, Addressing modes in
8085, Introduction to 8086.

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Recognizing the quirk ways

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1 Chapter 3 The 8085
Microprocessor Architecture.

2 The 8085 and Its Busses.

The 8085 is an 8-bit general purpose microprocessor that can address 64K Byte of memory. It has 40 pins and requires 5V power supply. It

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can run at a frequency of 3
MHz (or 5 MHz). The pins on
the chip can be grouped ...

PPT - Chapter 3 The 8085
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...

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Questions This section
focuses on "8086
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improve the Microprocessor
skills required for various
interviews (campus

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