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IC Design I | Finding CMOS Schematic from a simple layoutDesign a 4-1 multiplexer using NAND Gates What is a CMOS? (NMOS, PMOS) VLSI Fabrication Process nMOS Fabrication | nMOS FABRICATION IN VLSI DESIGN 3.2.8 Worked Examples: CMOS Logic Gates Magic VLSI Layout Tutorial - part 1 CMOS Fabrication Process (Animation) CMOS VLSI DESIGN USING MICROWIND DAY 3 1 CMOS VLSI DESIGN USING MICROWIND DAY 3 2

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Fourth Edition Neil H. E. Weste Macquarie University and The University of Adelaide David Money Harris Harvey Mudd College CMOS VLSI Design A Circuits and Systems Perspective Addison-Wesley Boston Columbus Indianapolis New York San Francisco Upper Saddle River

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