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per second,

while the design
with chip B will
have a

throughput of
 $32 \times 50 \text{ w.u./s} =$
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per second. So,
the total
throughput is

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B. This is

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cost-performance

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greater.

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PROBLEMS C*

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A

4 Solutions

Solution 4.1

4.1.1 The values
of the signals
are as follows:

RegWrite MemRead

ALUMux MemWrite

ALUOp RegMux

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Branch a. 1 0 0

(Reg) 0 Add 1

(ALU) 0 b. 1 1 1

(Imm) 0 Add 1

(Mem) 0 ALUMux

is the control signal that controls the Mux at the ALU input, 0 (Reg) selects the output of the register file and 1 (Imm)

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the computer
(CPU) will seem
faster to the
user with more
RAM to keep more
data and

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close at hand
for the CPU

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more storage and
quicker data
access in a
smaller, more
convenient
package.

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