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**Digital design** Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z *DIGITAL SYSTEM DESIGN OVERVIEW* Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y, and one output z is *Digital Design: Q. 1.13: Do the following conversion problems: (a) Convert decimal 27.315 to binary* *Digital Design: Q. 1.10: Convert the following binary numbers to hexadecimal and to decimal: (a), (b)* Q. 7.7: A  $16K \times 4$  memory uses coincident decoding by splitting the internal decoder into  $X \times$  selection Q. 4.35: Implement a full adder with two  $4 \times 1$  multiplexers.

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Q. 1.1: List the octal and hexadecimal numbers from 16 to 32. Using A and B for the last two digits Q. 4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that the AND or OR

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