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1. General description – NXP Semiconductors

32-bit ARM Cortex-M4/M0+ MCU; 104 kB SRAM; 512 kB flash, 3 x I2C, 2 x SPI, 4 x USART, 32-bit counter/ timers, SCTimer/PWM, 12-bit

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LPC5410x 3.3 Frequency.1: The maximum operating system clock on the LPC5410x device is limited to 96 MHz. Introduction: The LPC5410x data sheet specifies that the LPC5410x device can operate at CPU frequencies up to 100 MHz.

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Problem:

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NXP Semiconductors UM10850 Chapter 1:

LPC5410x Introductory information 1.4

Architectural overview The ARM Cortex-M4

includes three AHB-Lite buses, one system bus and the I-code and D-code buses. One bus is dedicated for instruction fetch (I-code), and one bus is dedicated for data access (D-code). The use of two core buses allows for simultaneous

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element14

Page 48 UM10850 NXP Semiconductors Chapter 4:
LPC5410x System configuration (SYSCON)

4.5.37.2 System PLL status register The read-only PLL0_STAT SYSPLLSTAT register provides the PLL lock status Remark: The lock status does not reliably indicate the PLL status for the following two configurations: spread-spectrum mode or fractional enabled or low input clock frequencies such as 32 kHz.

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asymmetric dual-core architecture with low power consumption for always on sensor applications.

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When in an active mode, developers can optimize power efficiency and throughput by choosing between the power-efficient Cortex-M0+ core for data collection, aggregation, and system task management, or the Cortex-M4

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core, which can complete processor-intensive algorithms, such as sensor fusion more quickly helping to reduce power consumed.

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NXP LPC5410x ARM microcontrollers are ARM

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Cortex-M4 MCUs with an optional Cortex-M0+ co-processor. The LPC5410x is designed for embedded applications and offers low power consumption, enhanced debug features, and a high level of support block integration. The ARM Cortex-M4 Lpc5410x Data Sheet Nxp Semiconductors

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1. General description The LPC51U68 are ARM Cortex-M0+ based microcontrollers for embedded applications. These devices include 96 KB of on-chip SRAM, 256 KB on-chip flash, full-speed USB

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