

## Modelsim Doentation

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### Modelsim-Doentation

Further verification of the detailed HDL design can be performed in the Mentor Graphics verification environment, leveraging the test vectors created at the model level, using tools such as ModelSim .

The MathWorks and Mentor Graphics Outline Joint-DO-254 Workflow for Model-Based Design

The checker is supported with Incisive NCVERLOG, VCS and Modelsim simulators ... Opperf2 is used to measure Bandwidth, Latency, etc. SOLV documentation can be referred to for the available options ...

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