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Answers 1) Explain how logical gates  
are controlled by Boolean logic? In  
Boolean algebra, the true state is  
denoted by the number one, referred  
as logic one or logic high. While, the  
false state is represented by the  
number zero, called logic zero or logic  
low.

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~~Answer~~ Instead of BJTs? Question2: What are the various regions of operation of MOSFET? How are those regions used? Question3: What is threshold voltage? Question4: What does it mean 'the channel is pinched off'?

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Question 33. What Is Local-skew, Global-skew,useful-skew Mean?

Answer : Local skew : The difference between the clock reaching at the launching flop vs the clock reaching the destination flip-flop of a timing-path.

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using 2 to 1 MUX. What does the setup time of a flip-flop depend upon? Come up with logic that counts number of ' 1 ' s in a 7 bit wide vector. You can only use combinational logic. Divide a clock by 3. You have an input vector where on ...

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VLSI Interview Questions and Answers :- 1. Why does the present VLSI circuits use MOSFETs instead of BJTs? Compared to BJTs, MOSFETs can be made very small as they occupy very small silicon area on IC chip and are relatively simple in terms of manufacturing.

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300+ TOP VLSI Design Interview Questions – Answers Question 1. What Are Four Generations Of Integration Circuits? Answer : SSI (Small Scale Integration) MSI (Medium Scale... SSI (Small Scale Integration) MSI (Medium Scale Integration) LSI (Large Scale Integration) VLSI (Very Large Scale Integration) ...



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~~Answers~~ just have to know the real deal to survive a job interview.

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Answer 2. Implement an 2-input AND gate using a 2x1 mux. Answer 3.

What is a multiplexer? Answer A multiplexer is a combinational circuit which selects one of many input signals and directs to the only output.

4. What is a ring counter? Answer A ring counter is a type of counter composed of a circular shift register.

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We are in process to add more questions. If you have any question that can be added to this section then please write to us with Question and detailed answer at [info@vlsiencyclopedia.com](mailto:info@vlsiencyclopedia.com) we would

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~~Answers~~ are two primary types of procedural blocks available in Verilog which are:

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